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## 21 [Automatic Ontology Generation Using Schema Information](#)

Shun-hong Sie, Jian-hua Yeh  
 December WIC '06: Proceedings of the 2006 IEEE/WIC/ACM International Conference on  
 2006 Web Intelligence  
**Publisher:** IEEE Computer Society

Full text available: [pdf\(343.67 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper discusses the automatic ontology generation process in a digital library. Traditional automatic ontology generation uses hierarchical clustering to group similar terms, and the result hierarchy is usually not satisfactory for human's recognition. ...

**Keywords:** ontology, concept hierarchy, hierarchical clustering, cluster partitioning, digital library.

**22** On building next generation data centers: energy flow in the information technology stack

Ratnesh K. Sharma, Rocky Shih, Cullen Bash, Chandrakant Patel, Philip Varghese, Mohandas Mekanapurath, Sankaragopal Velayudhan, Manu Kumar, V  
January Compute '08: Proceedings of the 1st Bangalore annual Compute conference 2008

**Publisher:** ACM

Full text available:  pdf(153.35 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The demand for data center solutions with lower total cost of ownership and lower complexity of management is driving the creation of next generation datacenters. The information technology industry is in the midst of a transformation to lower the cost ...

**Keywords:** data center management, energy efficiency, smart cooling, smart data center, sustainability

**23** An approximate global illumination system for computer generated films

Eric Tabellion, Arnauld Lamotte  
August SIGGRAPH '04: ACM SIGGRAPH 2004 Papers 2004

**Publisher:** ACM

Full text available:  pdf(819.51 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Lighting models used in the production of computer generated feature animation have to be flexible, easy to control, and efficient to compute. Global illumination techniques do not lend themselves easily to flexibility, ease of use, or speed, and have ...

**Keywords:** distributed ray tracing, global illumination, irradiance caching, micro-polygon, rendering

**24** Stochastic glossy global illumination on the GPU

Attila Barsi, László Szirmay-Kalos, Gábor Szilártó  
May SCCG '05: Proceedings of the 21st spring conference on Computer graphics 2005

**Publisher:** ACM

Full text available:  pdf(324.76 KB)

Additional Information: [full citation](#), [abstract](#), [references](#)

This paper presents an algorithm for the glossy global illumination problem, which runs on the Graphics Processing Unit (GPU). In order to meet the architectural limitations of the GPU, we apply randomization in the iteration scheme. Randomization allows ...

**25** A framework for malicious workload generation

 **Joel Sommers, Vinod Yegneswaran, Paul Barford**  
October **IMC '04: Proceedings of the 4th ACM SIGCOMM conference on Internet**  
2004 **measurement**

**Publisher:** ACM

Full text available:  [pdf\(600.78 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Malicious traffic from self-propagating worms and denial-of-service attacks constantly threatens the everyday operation of Internet systems. Defending networks from these threats demands appropriate tools to conduct comprehensive vulnerability assessments ...

**Keywords:** network intrusions, traffic generation

**26** Generation of design guarantees for interconnect matching

 **Andrew B. Kahng, Rasit Onur Topaloglu**  
March **SLIP '06: Proceedings of the 2006 international workshop on System-level**  
2006 **interconnect prediction**

**Publisher:** ACM

Full text available:  [pdf\(345.20 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Manufacturable design requires matching of interconnects which have equal nominal dimensions. New design rules are projected to bring guarantee rules for interconnect matching. In this paper, we present a methodology to generate additional guarantees ...

**Keywords:** design guarantee generation, interconnect matching

**27** Planning and improving global software development process using simulation

 **Siri-on Setamanit, Wayne Wakeland, David Raffo**  
May **GSD '06: Proceedings of the 2006 international workshop on Global software**  
2006 **development for the practitioner**

**Publisher:** ACM

Full text available:  [pdf\(248.77 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Global software development poses a number of challenges and difficulties as well as significant potential benefits. In order to be successful, companies need to adapt and improve their processes to support this kind of development. Strong project planning ...

**Keywords:** distributed development, global software development, hybrid simulation model, software process simulation modeling

**28** Templates and automation for temporal query generation

 Hong Liu, David P. Gluch

March ACM-SE 44: Proceedings of the 44th annual Southeast regional conference  
2006

**Publisher:** ACM

Full text available:  pdf(125.60 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper presents preliminary results in defining templates and a framework for an automation tool for generating temporal logic queries from the expected properties of requirements documents specified in natural Language. Specifically, we first extend ...

**Keywords:** architecture analysis & design language, computation tree logic, model checking

**29** Global data flow analysis by decomposition into primes

Ira R. Forman

September ICSCE '82: Proceedings of the 6th international conference on Software  
1982 engineering

**Publisher:** IEEE Computer Society Press

Full text available:  pdf(612.28 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

The concept of prime program is applied as a decomposition technique to the global data flow analysis problem. This is done both in the abstract and through the use of the live variables problem as an example. It is also shown how the prime program decomposition ...

**Keywords:** Compilers, Global data flow analysis, Prime program, Program optimization

**30** Statistical acquisition of content selection rules for natural language generation

Pablo A. Duboue, Kathleen R. McKeown

July Proceedings of the 2003 conference on Empirical methods in natural  
2003 language processing - Volume 10, Volume 10

**Publisher:** Association for Computational Linguistics

Full text available:  pdf(174.91 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

A Natural Language Generation system produces text using as input semantic data. One of its very first tasks is to decide which pieces of information to convey in the output. This task, called Content Selection, is quite domain dependent, requiring considerable ...

**31** Power optimization using divide-and-conquer techniques for minimization of the number of operations

Inki Hong, Miodrag Potkonjak, Ramesh Karri

October ACM Transactions on Design Automation of Electronic Systems (TODAES), 1999 Volume 4 Issue 4

**Publisher:** ACMFull text available:  pdf(278.45 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We introduce an approach for power optimization using a set of compilation and architectural techniques. The key technical innovation is a novel divide-and-conquer compilation technique to minimize the number of operations for general computations. Our ...

**Keywords:** code generation, transformations**32** Directional bias and non-uniformity in FPGA global routing architectures

Vaughn Betz, Jonathan Rose

January ICCAD '96: Proceedings of the 1996 IEEE/ACM international conference on 1997 Computer-aided design

**Publisher:** IEEE Computer SocietyFull text available:  pdf(78.32 KB)  Publisher Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

This paper investigates the effect of the prefabricated routing track distribution on the area-efficiency of FPGAs. The first question we address is whether horizontal and vertical channels should contain the same number of tracks (capacity), or if there ...

**Keywords:** Field-Programmable Gate Arrays (FPGAs), Global Routing, Placement**33** Automatic generation of rule-based constraint solvers over finite domains

Slim Abdennadher, Christophe Rigotti

April ACM Transactions on Computational Logic (TOCL), Volume 5 Issue 2 2004

**Publisher:** ACMFull text available:  pdf(170.72 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A general approach to implement propagation and simplification of constraints consists of applying rules over these constraints. However, a difficulty that arises frequently when writing a constraint solver is to determine the constraint propagation ...

**Keywords:** Rule-based constraint programming, finite domains, generation of solvers

**34** [Global multimedia system design exploration using accurate memory organization feedback](#)

Arnout Vandecappelle, Miguel Miranda, Erik Brockmeyer, Francky Catthoor, Diederik Verkest  
June DAC '99: Proceedings of the 36th ACM/IEEE conference on Design automation  
1999

**Publisher:** ACM

Full text available:  [pdf\(70.73 KB\)](#)

Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**35** [Automatic network generation for system-on-chip communication design](#)

Dongwan Shin, Andreas Gerstlauer, Rainer Dömer, Daniel D. Gajski  
September 2005 CODES+ISSS '05: Proceedings of the 3rd IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis

**Publisher:** ACM

Full text available:  [pdf\(206.97 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

With growing system complexities, system-level communication design is becoming increasingly important and advanced, network-oriented communication architectures become necessary. In this paper, we extend previous work on automatic communication refinement ...

**Keywords:** communication synthesis, system level design

**36** [Towards second and third generation web-based multimedia](#)

Jacco van Ossenbruggen, Joost Geurts, Frank Cornelissen, Lynda Hardman, Lloyd Rutledge  
April 2001 WWW '01: Proceedings of the 10th international conference on World Wide Web

**Publisher:** ACM

Full text available:  [pdf\(563.48 KB\)](#)

Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Keywords:** SMIL, XML, XSLT, multimedia, transformations

### 37 Dynamic generation of web migratory interfaces

 Renata Bandelloni, Giulio Mori, Fabio Paternò

September 2005 MobileHCI '05: Proceedings of the 7th international conference on Human computer interaction with mobile devices & services

**Publisher:** ACM

Full text available:  pdf(1.72 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

In this paper, we present a solution for dynamic generation of Web user interfaces that can dynamically migrate among different platforms. The solution is based on a migration/proxy server able to automatically convert a desktop service into a service ...

**Keywords:** automatic transformations, migratory interfaces, model-based design, ubiquitous environments

### 38 Global grid monitoring: the EGEE/WLCG case

 Alexandre Nóbrega Duarte, Piotr Nyczek, Antonio Retico, Domenico Vicinanza

June 2007 GMW '07: Proceedings of the 2007 workshop on Grid monitoring

**Publisher:** ACM

Full text available:  pdf(307.31 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Grids have the potential to revolutionize computing by providing ubiquitous, on demand access to computational services and resources. However, grid systems are extremely large, complex and prone to failures. In this paper we present a tool able to ...

**Keywords:** EGEE, WLCG, grid monitoring, service availability monitoring, software testing

### 39 Anisotropic voronoi diagrams and guaranteed-quality anisotropic mesh generation

 Francois Labelle, Jonathan Richard Shewchuk

June 2003 SCG '03: Proceedings of the nineteenth annual symposium on Computational geometry

**Publisher:** ACM

Full text available:  pdf(423.41 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

We introduce *anisotropic Voronoi diagrams*, a generalization of multiplicatively weighted Voronoi diagrams suitable for generating guaranteed-quality meshes of domains in which long, skinny triangles are required, and where the desired anisotropy ...

**Keywords:** anisotropic Voronoi diagram, anisotropic mesh generation

## 40 Platform-Based Testbench Generation

R. Henftling, A. Zinn, M. Bauer, W. Ecker, M. Zambaldi

March DATE '03: Proceedings of the conference on Design, Automation and Test in 2003 Europe - Volume 1, Volume 1

**Publisher:** IEEE Computer Society

Full text available:  [pdf\(159.87 KB\)](#)  [Publisher Site](#)

Additional Information: [full citation](#), [abstract](#), [references](#),  
[cited by](#), [index terms](#)

This paper presents a new technology that accelerates system verification. In a real life example, we achieved a speed-up of a factor of about 5000. The key for this speed-up is a configurable, synthesizable testbench architecture, which can be completely ...

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